

Substitute for form 1449

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

Application No. TBA

Applicant: Gunzinger

Filing Date: Herewith

Title: Method of Rapid Communication  
within a Parallel Computer System  
and a Parallel Computer System  
Operated by the Method

Attorney Docket No.: FREI.P-033US-2

J1033 U.S. PTO  
09/954596  
09/12/01

Page 1 of 1

**U.S. PATENT DOCUMENTS**

Examiners Initials	U S Patent No.	Name of Persons or applicant	Date of Publication of Cited Document
RLE	5,117,350	Parrish et al.	May 26, 1992
/			

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RLE	WO 95/16239	Cray Research	June 16, 1995
RCE	WO 91/10200	Maspar Computer Corporation	July 11, 1991
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**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

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RLE	Keeping It Simple, by Tom R. Halfhill, Byte, October 1997, pages 51 - 52
RLE	Applications Considerations in the System Design of Highly Concurrent Multiprocessors, by Stephen F. Lundstrom, pp. 1292 - 1309, 8092 IEEE Transactions on Computers, C-36 (1987), No. 11, New York, NY

This Information Disclosure Citation List is being submitted as a substitute for Form PTO-1449. The Examiner is requested to place his or her initials on the lines adjacent to the citations to indicate that the reference has been considered. The Examiner is further requested to fill in his or her name and the date the information was considered in blocks at the bottom of this substitute for Form PTO-1449.

Richard Ellis  
Examiner Signature

6/27/2002  
Date Considered

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**



Application No. 09/954,596  
Applicant: Gunzinger  
Filing Date: September 12, 2001  
Title: Method of Rapid Communication  
within a Parallel Computer System  
and a Parallel Computer System  
Operated by the Method  
Attorney Docket No.: FREI.P-033US-2

Page 1 of 2

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Page 2 of 2

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RUE	Motlagh, B.S. et al., <i>Memory latency in distributed shared-memory multiprocessors</i> , Proceedings IEEE Southeastcon '98, pp. 24-26, April 1998.
RUE	Kuskin, J. et al., <i>The Stanford FLASH multiprocessor</i> , Proceedings of the 24 <sup>th</sup> Annual International Symposium on Computer Architecture, IEEE, pp 18-21, April 1994
RUE	Prete, C.A. et al., <i>Reducing coherence-related overhead in multiprocessor systems</i> , Proceedings of Euromicro Workshop on Parallel and Distributed Processing, pp 25-27, January 1995.

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